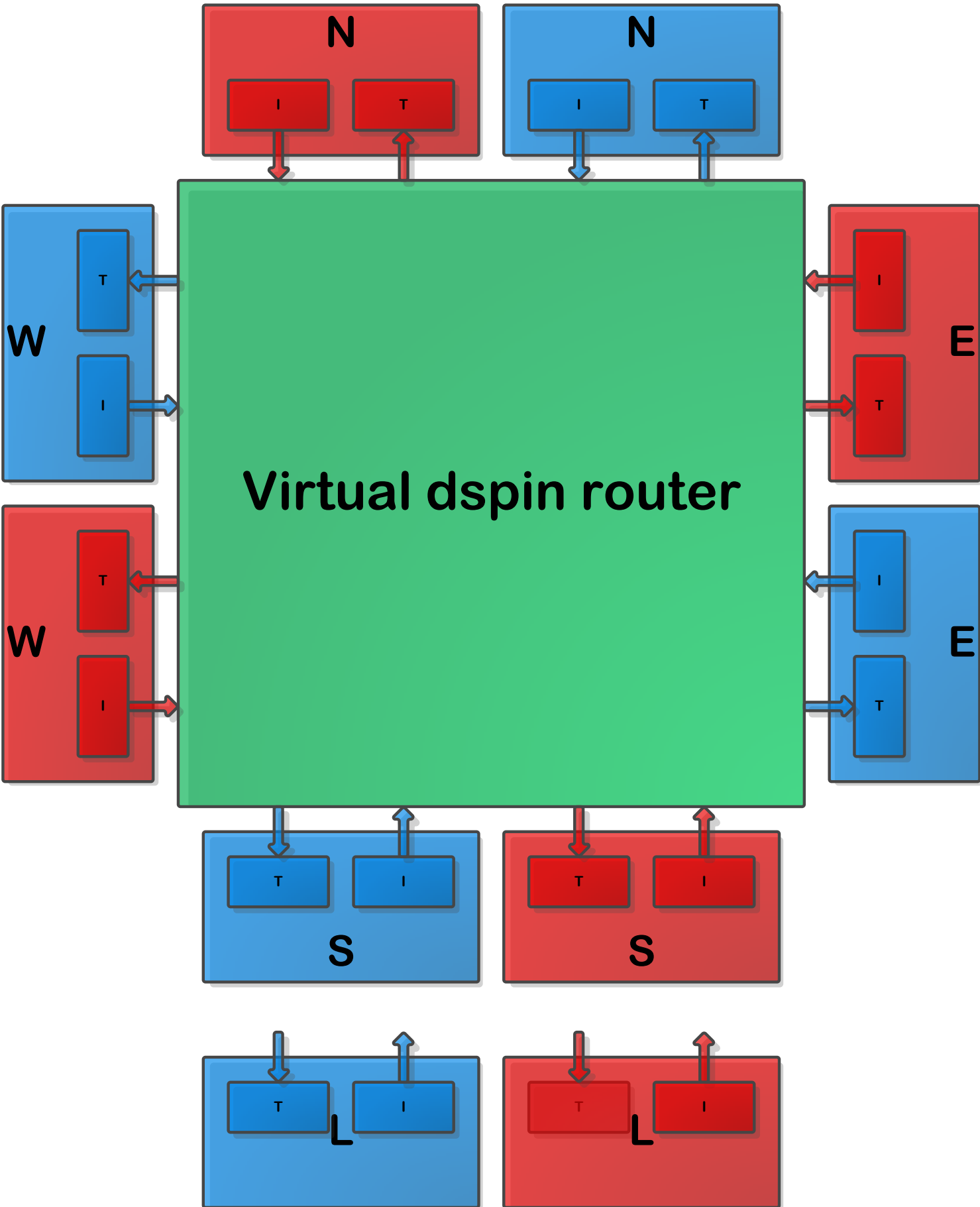
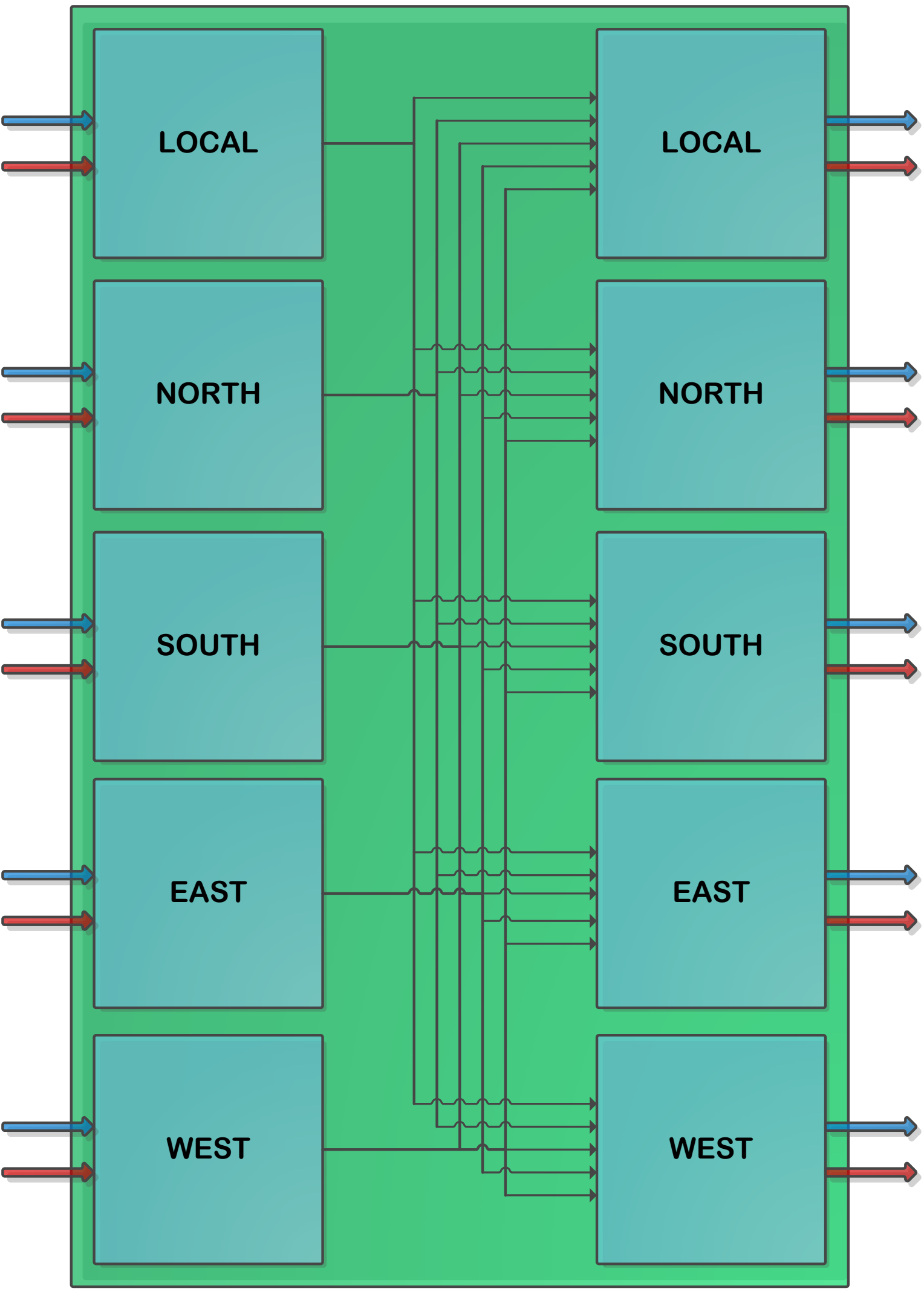


# Virtual Dspin Router - outside

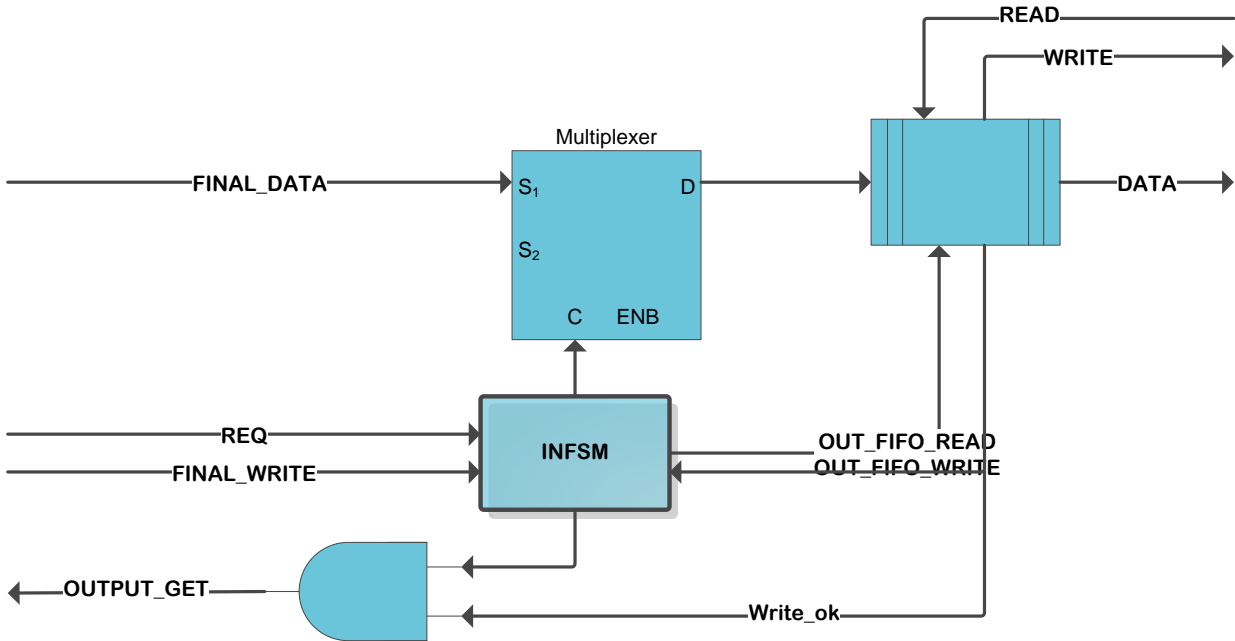


# Virtual Dspin Router - inside

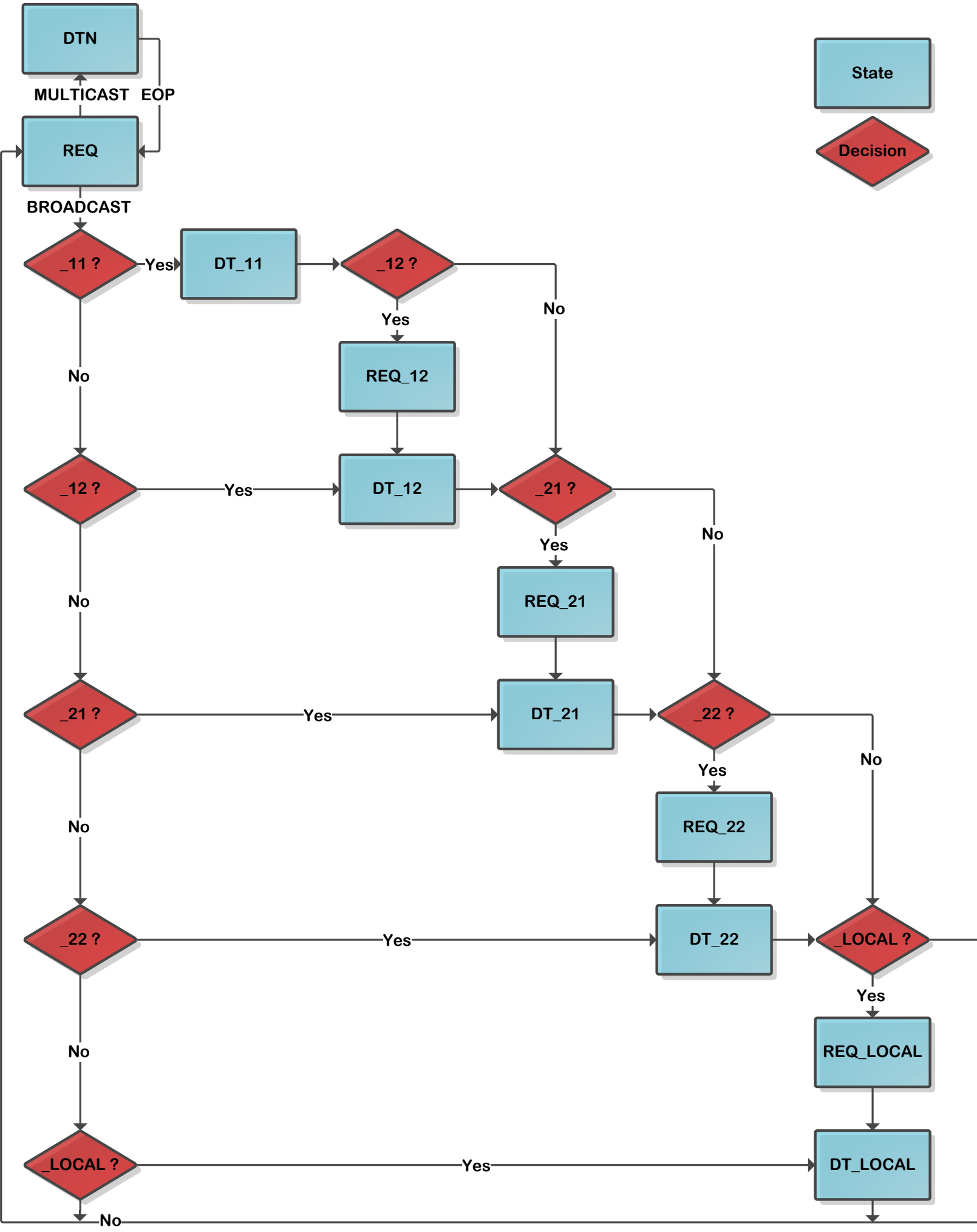




# Virtual Dspin Router - output



# Virtual Dspin Router – input INFSM



# Virtual Dspin Router – output INFSM

