

DspinRouter

1) Functional Description

This hardware component is the generic router for the DSPIN micronetwork. It can be used in conjunction with the [VciDspinTargetWrapper](#) and the [VciDspinInitiatorWrapper](#) components to build a VCI compliant DSPIN micro-network.

2) Component definition & usage

3) CABA Implementation

CABA sources

- interface :
source:trunk/soclib/soclib/module/network_component/vci_dspin_initiator_wrapper/caba/source/include/dspin_router.h
- implementation :
source:trunk/soclib/soclib/module/network_component/vci_dspin_initiator_wrapper/caba/source/src/dspin_router.cpp

CABA Constructor parameters

CABA Ports

4) TLM-T implementation

There is no TLM-T implementation for the DSPIN network. (You can use the [VciVgmn](#) generic interconnect)