

HalfGatewayTarget

1) Functional Description

This hardware component is a subcomponent of the dedicated interface : [RingGateway](#). It handles outcoming responses and incoming commands from and to the ring interconnect to which it is attached.

2) Component definition & usage

source:trunk/soclib/soclib/module/network_component/half_gateway_target/caba/metadata/half_gateway_target.sd

3) CABA implementation

CABA sources

- interface :
source:trunk/soclib/soclib/module/network_component/half_gateway_target/caba/source/include/half_gateway_target.h
- implementation :
source:trunk/soclib/soclib/module/network_component/half_gateway_target/caba/source/src/half_gateway_target.cpp

CABA Constructor parameters

```
HalfGatewayInitiator(sc_module_name      insname,           // instance name
                    bool                  alloc_target,       // default target token owner
                    const int              &cmd_fifo_depth,  // command fifo depth
                    const soclib::common::MappingTable &mt,   // mapping table
                    const soclib::common::IntTab &ringid,     // global subsystem index
                    bool local);                               // command routing parameter
```

CABA ports

- sc_in<bool> p_clk; *Global system clock*
- sc_in<bool> p_resetn; *Global system reset*
- soclib::caba::RingIn p_ring_in; *Ring input port*
- soclib::caba::RingOut p_ring_out; *Ring output port*
- soclib::caba::GateTarget p_gate_target; *Port to gate initiator*

4) TLMT implementation

The TLM-T implementation is not available yet.