

ISS Wrapper

This hardware component is a generic ISS wrapper. It can embed any ISS based on source:trunk/soclib/systemc/include/common/iss/iss.h.

Template parameters

IssWrapper takes the wrapped ISS as template parameter.

```
soclib::caba::IssWrapper<typename iss_t>
```

For instance if wrapping a Mips, little endian form:

```
soclib::caba::IssWrapper<soclib::common::MipsElIss>
```

Constructor parameters

- name: The cell name
- ident: The processor number

```
IssWrapper( sc_module_name name, int ident );
```

Again, with a mipsel, we get:

```
soclib::caba::IssWrapper<soclib::common::MipsElIss> mips0( "mips0", 0 );
```

Ports

- sc_in<bool> **p_resen** : Global system reset
- sc_in<bool> **p_clk** : Global system clock
- sc_in<bool> **p_irq!**[N] : The interrupts. N is defined by the wrapped ISS.
- soclib::caba::IcacheProcesssorPort **p_icache** : Instruction cache interface to the VciXcache
- soclib::caba::DcacheProcesssorPort **p_dcache** : Data cache interface to the VciXcache