TMS320C62 Processor Functional Description

This hardware component This hardware component is only an ISS, which should be wrapped with an IssWrapper.

Component definition

Available in source:trunk/soclib/soclib/lib/tms320c6x/metadata/tms320c62.sd

Usage

TMS320C62 has no parameters.

```
Uses( 'tms320c62')
```

NiosII Processor ISS Implementation

The implementation is in

- source:trunk/soclib/soclib/lib/tms320c6x/include/iss/tms320c62.h
- source:trunk/soclib/soclib/lib/tms320c6x/src/iss/tms320c62.cpp
- source:trunk/soclib/soclib/lib/tms320c6x/src/iss/tms320c62_decoding.cpp
- source:trunk/soclib/soclib/lib/tms320c6x/src/iss/tms320c62_instructions.cpp

Template parameters

This component has no template parameters.

Constructor parameters

```
TMS320C62(
     sc_module_name name,   //  Instance Name
     int  ident);           // processor id
```

Visible registers

The following internal registers define the processor internal state, and can be inspected:

Interrupts

Ports

None, it is to the wrapper to provide them.