

VciDspinInitiatorWrapper

Functional Description

This hardware component connect a VCI initiator to a DSPIN micronetwork. It can be used in conjunction with the [VciDspinTargetWrapper](#) and the [DspinRouter](#) components to build a VCI compliant DSPIN micro-network. It translates the VCI packet format to the DSPIN packet format.

The DSPIN network on chip is a distributed network, with a 2D mesh topology. It has been designed for shared memory clusterized, multi-processors architectures. It supports the GALS (Globally Asynchronous Locally Synchronous) approach.

Component definition

Usage

CABA Implementation

- interface :
[source:trunk/soclib/soclib/module/network_component/vci_dspin_initiator_wrapper/caba/source/include/vci_dspin_i](#)
- implementation :
[source:trunk/soclib/soclib/module/network_component/vci_dspin_initiator_wrapper/caba/source/src/vci_dspin_initiat](#)

TLM-T implementation

There is no TLM-T implementation for the DSPIN network. (You can use the [VciVgmn](#) generic interconnect)

Template parameters

Constructor parameters

Ports