

VciDspinInitiatorWrapper Functional Description

This hardware component connects a VCI target to a DSPIN micro-network. It can be used in conjunction with the **VciDspinInitiatorWrapper** and the **DspinRouter** components to build a VCI compliant DSPIN micro-network.

The DSPIN network on chip is a distributed network, with a 2D mesh topology. It has been designed for shared memory clusterized, multi-processors architectures. It supports the GALS (Globally Asynchronous Locally Synchronous) approach.

Component definition

Usage

CABA Implementation

The caba implementation is in

- [source:trunk/soclib/soclib/module/network_component/vci_dspin_initiator_wrapper/caba/source/include/vci_dspin_i](#)
- [source:trunk/soclib/soclib/module/network_component/vci_dspin_initiator_wrapper/caba/source/src/vci_dspin_initiat](#)

TLM-T implementation

There is no TLM-T implementation for the DSPIN network. (You can use the VciVgmn generic VCI interconnect)

Template parameters

None

Constructor parameters

Ports