

# VciRam

## 1) Functional Description

This VCI target is an embedded SRAM controller. This hardware component handles independent memory segments. Each segment is defined by a base address and a size (number of bytes). Both the base and the size parameters must be multiple of 4. The segments allocated to a given instance of this component are defined in the [Mapping Table](#).

Each segment is implemented as a dynamically allocated array in the constructor.

A MultiRam will initialize its segments from a binary if an ElfLoader? is attached to it.

## 2) DSX Component definition & usage

[source:trunk/soclib/soclib/module/internal\\_component/vci\\_ram/caba/metadata/vci\\_ram.sd?](#)

See [SoclibCc/VciParameters](#)

```
Uses( 'vci_ram', **vci_parameters )
```

## 3) CABA Implementation

### CABA sources

- interface : [source:trunk/soclib/soclib/module/internal\\_component/vci\\_ram/caba/source/include/vci\\_ram.h?](#)
- implementation :  
[source:trunk/soclib/soclib/module/internal\\_component/vci\\_ram/caba/source/src/vci\\_ram.cpp?](#)

### CABA Constructor parameters

- Uninitialized VciRam

```
VciRam(
    sc_module_name name,                                // Instance name
    const soclib::common::IntTab &index,               // Target index
    const soclib::common::MappingTable &mt)           // Mapping Table
```

- Elf-Initialized VciRam

You may load a binary file, by creating a loader:

```
soclib::common::ElfLoader loader( "a.out" );
VciMultiRam(
    sc_module_name name,                                // Instance name
    const soclib::common::IntTab &index,               // Target index
    const soclib::common::MappingTable &mt,             // Mapping Table
    soclib::common::ElfLoader &loader); // Loader
```

On reset, any loadable segment in ELF file will be reloaded .

## CABA Ports

- sc\_in<bool> **p\_resetn** : hardware reset
- sc\_in<bool> **p\_clk** : clock
- soclib::common::VciTarget<vci\_param> **p\_vci** : The VCI port

## 4) TLM-T Implementation

### TLM-T sources

- interface : [source:trunk/soclib/soclib/module/internal\\_component/vci\\_ram/tlmt/source/include/vci\\_ram.h?](#)
- implementation :  
[source:trunk/soclib/soclib/module/internal\\_component/vci\\_ram/tlmt/source/src/vci\\_ram.cpp?](#)