

VciRingInitiatorWrapper

1) Functional Description

This hardware component connect a VCI initiator to a RING interconnect. It can be used in conjunction with the [VciRingTargetWrapper](#) and the [VciRingRegister?](#) components to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

2) Component definition & usage

3) CABA Implementation

CABA sources

- interface :
[source:trunk/soclib/soclib/module/network_component/vci_ring_initiator_wrapper/caba/source/include/vci_ring_initiator_wrapper.h](#)
- implementation :
[source:trunk/soclib/soclib/module/network_component/vci_ring_initiator_wrapper/caba/source/src/vci_ring_initiator_wrapper.c](#)

CABA Constructor parameters

```
VciRingInitiatorWrapper(sc_module_name insname); // instance name
```

CABA Ports

sc_in<bool>	p_clk;
sc_in<bool>	p_resen;
soclib::caba::RingINPort<vci_param>	p_ri;
soclib::caba::RingOUTPort<vci_param>	p_ro;
soclib::caba::VciTarget<vci_param>	p_vci;

4) TLM-T Implementation

The TLM-T implementation is not yet available. You can use the [VciVgmn](#) generic interconnect.