

VciRingInitiatorWrapper

1) Functional Description

This hardware component connects a VCI initiator to a RING interconnect. It can be used in conjunction with the [VciRingTargetWrapper](#) component to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

2) Component definition & usage

[source:trunk/soclib/soclib/module/network_component/vci_ring_initiator_wrapper/caba/metadata/vci_ring_initiator_wrapper](#)

3) CABA Implementation

CABA sources

- interface :

[source:trunk/soclib/soclib/module/network_component/vci_ring_initiator_wrapper/caba/source/include/vci_ring_initiator.h](#)

- implementation :

[source:trunk/soclib/soclib/module/network_component/vci_ring_initiator_wrapper/caba/source/src/vci_ring_initiator.cpp](#)

CABA Constructor parameters

```
VciRingInitiatorWrapper(sc_module_name insname,                                     // instance name
                       bool      alloc_init,           // default token owner
                       const int &wrapper_fifo_depth, // command and response fifo
                       const soclib::common::MappingTable &mgt, // mapping table
                       const soclib::common::IntTab &ringid, // Global subsystem index
                       const int &srcid);             // attached initiator index
```

CABA Ports

```
* sc_in<bool>                                p_clk;          // Global System Clock
* sc_in<bool>                                p_resetn;       // Global System reset
* soclib::caba::!RingIn                         p_ring_in;     // Ring input port
* soclib::caba::!RingOut                        p_ring_out;    // Ring output port
* soclib::caba::!VciTarget<vci_param>          p_vci;         // VCI target port
```

4) TLM-T Implementation

The TLM-T implementation is not yet available.