

VciRingTargetWrapper

1) Functional Description

This hardware component connects a VCI Target to a RING interconnect. It can be used in conjunction with the [VciRingInitiatorWrapper](#) component to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

2) Component definition & usage

[source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/metadata/vci_ring_target_wrapper.sd?](#)

3) CABA Implementation

CABA sources

- interface :
[source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/source/include/vci_ring_target](#)
- implementation :
[source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/source/src/vci_ring_target_wra](#)

CABA Constructor parameters

```
VciRingTargetWrapper(sc_module_name      insname,           // instance Name
                    bool                  alloc_target,       // default token owner
                    const int              &wrapper_fifo_depth, // command and response fif
                    const soclib::common::MappingTable &mt,    // mapping Table
                    const soclib::common::IntTab &ringid,      // global subsystem Index
                    const int &tgtid);                          // attached target index
```

CABA Ports

- `sc_in<bool> p_clk;` *global System Clock*
- `sc_in<bool> p_rese``tn;` *global System reset*
- `soclib::caba::RingIn p_ring_in;` *ring input port*
- `soclib::caba::RingOut p_ring_out;` *ring output port*
- `soclib::caba::VciInitiator<vci_param> p_vci;` *VCI initiator port*

4) TLM-T Implementation

The TLM-T implementation is not yet available.