

# VciRingTargetWrapper

## 1) Functional Description

This hardware component connects a VCI Target to a RING interconnect. It can be used in conjunction with the [VciRingInitiatorWrapper](#) and the [RingRegister?](#) components to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

## 2) Component definition & usage

[source:trunk/soclib/soclib/module/network\\_component/vci\\_ring\\_target\\_wrapper/caba/metadata/vci\\_ring\\_target\\_wrapper.sd?](#)

## 3) CABA Implementation

### CABA sources

- interface :  
[source:trunk/soclib/soclib/module/network\\_component/vci\\_ring\\_target\\_wrapper/caba/source/include/vci\\_ring\\_target](#)
- implementation :  
[source:trunk/soclib/soclib/module/network\\_component/vci\\_ring\\_target\\_wrapper/caba/source/src/vci\\_ring\\_target\\_wra](#)

### CABA Constructor parameters

```
VciRingTargetWrapper(sc_module_name      insname,           // Instance Name
                    bool                  alloc_target,       // Default token owner
                    const int             &wrapper_fifo_depth, // command and response fif
                    const soclib::common::MappingTable &mt,   // Mapping Table
                    const soclib::common::IntTab &ringid,     // Global subsystem Index
                    const int &tgtid);                        // Attached target index
```

### CABA Ports

- `sc_in<bool> p_clk;` *Global System Clock*
- `sc_in<bool> p_rese``tn;` *Global System reset*
- `soclib::caba::RingIn p_ring_in;` *Ring input port*
- `soclib::caba::RingOut p_ring_out;` *Ring output port*
- `soclib::caba::VciInitiator<vci_param> p_vci;` *VCI Initiator port*

## 4) TLM-T Implementation

The TLM-T implementation is not yet available.