

VciRingTargetWrapper

Functional Description

This hardware component connects a VCI Target to a RING interconnect. It can be used in conjunction with the [VciRingInitiatorWrapper](#) and the [RingRegister?](#) components to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

Component definition

Usage

CABA Implementation

- interface :
[source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/source/include/vci_ring_target_wrapper.h](#)
- implementation :
[source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/source/src/vci_ring_target_wrapper.cpp](#)

TLM-T Implementation

The TLM-T implementation is not yet available. You can use the [VciVgmn](#) generic interconnect.

Template parameters

Constructor parameters

Ports