

# VciRingTargetWrapper

## 1) Functional Description

This hardware component connects a VCI Target to a RING interconnect. It can be used in conjunction with the [VciRingInitiatorWrapper](#) and the [RingRegister?](#) components to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

## 2) Component definition & usage

## 3) CABA Implementation

### CABA sources

- interface :  
[source:trunk/soclib/soclib/module/network\\_component/vci\\_ring\\_target\\_wrapper/caba/source/include/vci\\_ring\\_target\\_wrapper.h](#)
- implementation :  
[source:trunk/soclib/soclib/module/network\\_component/vci\\_ring\\_target\\_wrapper/caba/source/src/vci\\_ring\\_target\\_wrapper.cpp](#)

### CABA Constructor parameters

### CABA Ports

## TLM-T Implementation

The TLM-T implementation is not yet available. You can use the [VciVgmn](#) generic interconnect.