VciRingTargetWrapper

1) Functional Description

This hardware component connect a VCI Target to a RING interconnect. It can be used in conjunction with the VciRingInitiatorWrapper and the VciRingRegister? components to build a RING interconnect. This type of interconnect is well suited for FPGA implementations.

2) Component definition & usage

3) CABA Implementation

CABA sources

- interface : source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/source/include/vci_ring_target
- implementation : source:trunk/soclib/soclib/module/network_component/vci_ring_target_wrapper/caba/source/src/vci_ring_target_wra

CABA Constructor parameters

```
VciRingTargetWrapper( sc_module_name name);   //  Instance Name
```

CABA Ports

- sc_in<bool> p_clk; *Global System Clock*
- sc_in<bool> p_resetn; *Global System reset*
- soclib::caba::RingINPort<vci_param> p_ri; *Ring input port*
- soclib::caba::RingOUTPort<vci_param> p_ro; *Ring output port*
- soclib::caba::VciInitiator<vci_param> p_vci; *VCI Initiator port*

4) TLM-T Implementation

The TLM-T implementation is not yet available. You can use the VciVgmn generic interconnect.