

SoCLib Components General Index

VCI Targets

- VciLocks : A memory mapped locks controller
- VciMultiRam : A multi-segment embedded Ram controller
- VciMultiTty : A memory mapped multi-TTY controller
- VciMultiTimer : A memory mapped multi-Timer controller
- VciIcu : A memory mapped interrupt controller

VCI Initiators

Processors

- Mips : A Mips R3000 processor core

Dedicated coprocessors

Interconnects

- vci_vgmn : A VCI advanced generic micro-network
- vci_pi_initiator_wrapper : A VCI-PIBUS protocol converter for a VCI initiator
- vci_pi_target_wrapper : A VCI-PIBUS protocol converter for a VCI target
- piibus_bcu : A PIBUS controller