

!SoCLib !Components

targets

- vci_locks : A memory mapped locks controller
- vci_multi_ram : A multi-segment embedded Ram controller
- vci_multi_tty : A memory mapped multi-TTY controller
- vci_multi_timer : A memory mapped multi-Timer controller
- vci_icu : A memory mapped interrupt controler

processors

interconnects

- vci_vgmn : A VCI advanced generic micro-network
- vci_pi_initiator_wrapper : A VCI-PIBUS protocol converter for a VCI initiator
- vci_pi_target_wrapper : A VCI-PIBUS protocol converter for a VCI target
- pibus_bcu : A PIBUS arbitration controller