

SoCLib Components General Index

VCI Targets

- VciMultiRam : A multi-segment embedded Ram controller
- VciMultiTty : A memory mapped multi-TTY controller
- VciMultiTimer : A memory mapped multi-Timer controller
- VciIcu : A memory mapped interrupt controller
- VciLocks : A memory mapped locks controller

VCI Initiators

- VciXcache : A generic data & instruction cache controller for RISC processors

Processors

- Mips : A Mips R3000 processor core

Dedicated coprocessors

VCI Interconnects

- VciVgmn : A VCI advanced generic micro-network
- VciPiInitiatorWrapper : A VCI-PIBUS protocol converter for a VCI initiator
- VciPiTargetWrapper : A VCI-PIBUS protocol converter for a VCI target
- PibusBcu : A PIBUS controller

Common utilities

- TtyWrapper : A simulator-side TTY abstraction tool, used by VciMultiTty
- MappingTable : A tool to declare and list all memory segments in a platform