

1. About SoCLib design
 1. TLM-DT
 2. ISS design
 3. Fast CABA simulation
2. About studies evaluating modeling of hardware components
 1. NoC
3. Using SoCLib as virtual prototype
 1. MWMMR
 2. Packet networks

About SoCLib design

TLM-DT

- Aline Vieira-de-Mello, Isaac Maia, Alain Greiner, François Pêcheux
Parallel Simulation of SystemC TLM 2.0 Compliant MPSoC on SMP Workstations
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Fast CABA simulation

- Richard Buchmann, Alain Greiner
A Fully Static Scheduling Approach for Fast Cycle Accurate SystemC Simulation of MPSoCs
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About studies evaluating modeling of hardware components

NoC

- Alain Greiner, Sami Taktak, Zhen Zhang
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Using SoCLib as virtual prototype

MWMMR

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Packet networks

- Daniela Genius, Alix Munier Kordon, Khouloud Zine el Abidine
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