

MWMR

General presentation

The MWMR communication middleware implements a generic inter-task communication mechanism for shared memory multi-processors architectures. This protocol has been designed to support both communication between *software tasks* (running on a programmable processor), and *hardware tasks*, implemented as dedicated hardware coprocessors.

MWMR stands for *Multi Writers, Multi-Readers*. The MWMR channel itself is implemented as a software FIFO, that can have several producers, and several consumers. Each MWMR communication channel is protected by a dedicated *lock*, for exclusive access.

More Information

You can obtain more detailed information, and download the code [here](#)