

Welcome to SoCLib's development Trac

1. A) SoCLib Library
2. B) SoCLib Tools
3. C) SoCLib Documentation
  1. Installation, usage
  2. Writing and design guides
  3. Miscellaneous
4. D) SoCLib project

## A) SoCLib Library

- SoCLib Components General Index : contains documentation about the hardware components (IP cores) available in the SoCLib library.
- Adding new components to the library : the rules to follow to add a new IP core to the library.

## B) SoCLib Tools

- ?DSX : Design Space Exploration tool
- ?SystemCASS : Fast SystemC simulation kernel
- ?SoCView : Simulation kernel for debug and instrumentation
- ?Mutek : OS kernel for embedded MPSoCs

## C) SoCLib Documentation

### Installation, usage

- Installation Notes : how to install the SoCLib platform on your computer
- Soclib Cc is the current build system for SoCLib platforms

### Writing and design guides

- General SoCLib Rules : general rules regarding the SoCLib components.
- CABA Writing Rules : rules to write CABA simulation models.
- TLM-T Writing Rules : rules to write TLM-T simulation models.
- Processor Modeling : a general method to write generic processor models.

### Miscellaneous

- Critères Pour Plate-Forme TLM-T : criteria defined for writing TLM-T simulation models.
- Comparaison licences LGPL & BSD
- SoclibCc/DesignGuide is an attempt to justify the choices made in soclib-cc

## D) SoCLib project

- Here are the Models of documents to be used?
- Here are the Minutes of the SoCLib meetings?
- Here is the archive of the development mailing-list : <http://www.soclib.fr/www>
- You may follow contractual roadmaps on the ANR/SoCLib project's Trac.