

## VciDspin

The VciDspin interconnect is a distributed network on chip, with a 2D mesh topology, providing the system designer a fully scalable bandwidth. It is composed of three components :

- [VciDspinInitiatorWrapper](#) : A VCI-DSPIN protocol converter for a VCI initiator.
- [VciDspinTargetWrapper](#) : A VCI-DSPIN protocol converter for a VCI target.
- [DspinRouter](#) : The generic DSPIN router.