

Welcome to SoCLib's development Trac

1. [A\) SoCLib Library](#)
 1. [Components documentation](#)
2. [B\) SoCLib Tools](#)
3. [C\) SoCLib Documentation](#)
 1. [Installation, usage](#)
 2. [Writing and design guides](#)
4. [D\) SoCLib project](#)

A) SoCLib Library

Components documentation

- [SoCLib Components General Index](#) : contains documentation about SoCLib components

B) SoCLib Tools

- [?DSX](#) : Design Space eXploration tool
- [?SystemCASS](#) : Fast SystemC simulation kernel
- [?SoCView](#) : Simulation kernel for debug and instrumentation
- [?Mutek](#) : OS kernel for embedded MPSoCs

C) SoCLib Documentation

Installation, usage

- [Installation Notes](#) : how to install the SoCLib platform on your computer
- [Soclib Cc](#) is the current build system for SoCLib platforms

Writing and design guides

- [General SoCLib Rules](#) : defines general rules regarding the SoCLib components.
 - [CABA Writing Rules](#) : helps to write efficient CABA simulation models.
 - [Critères Pour Plate-Forme TLM/T](#) : the criteria defined for writing TLM/T simulation models.
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- [SoclibCc/DesignGuide](#) is an attempt to justify the choices made in soclib-cc

D) SoCLib project

- Here are the Minutes of the SoCLib meetings?
- Here is the archive for the development mailing-list : <http://www.soclib.fr/wws>
- You may follow contractual roadmaps on the [ANR/SoCLib project's Trac](#).